

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

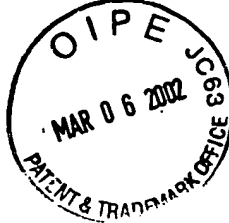
In re application of:

Shota KITAMURA et al.

Serial No: 09/392,865

Filed: September 9, 1999

For: NON-VOLATILE SEMICONDUCTOR
MEMORY DEVICE AND ITS
MANUFACTURING METHOD



Art Unit: 2811

Examiner: Thien F. Tran

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to:

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Date

AMENDMENT UNDER 37 C.F.R. § 1.116

Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

In response to the Final Office Action dated December 28, 2001, please
amend the above-identified application as follows:

IN THE CLAIMS:

Rewrite claim 16 as follows:

16. (Twice Amended) A nonvolatile semiconductor memory device
comprising:
a semiconductor substrate;
memory transistors formed on said semiconductor substrate to perform
nonvolatile storage of an electric charge in accordance with data, each of said